

UMD ProtoLink Peripheral Interface

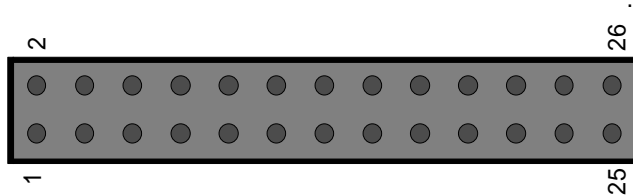
This document defines the UMD ProtoLink Peripheral Interface on a 26 way header.

1.1 Pin Assignment

| Pin Number | Signal Name | Signal Direction | Function |
|------------|-------------|------------------|------------------------------------|
| 1 | +5V | | +5V Regulated Power |
| 2 | GND | | Ground |
| 3 | A0 | OP | Address bit 0 |
| 4 | EXTRES | OP | External Reset active high |
| 5 | E1 | OP | Enable 1 active high |
| 6 | R/W | OP | Read active high /Write active low |
| 7 | D1 | I/O | Data bit 1 |
| 8 | D0 | I/O | Data bit 0 |
| 9 | D3 | I/O | Data bit 3 |
| 10 | D2 | I/O | Data bit 2 |
| 11 | D5 | I/O | Data bit 5 |
| 12 | D4 | I/O | Data bit 4 |
| 13 | D7 | I/O | Data bit 7 |
| 14 | D6 | I/O | Data bit 6 |
| 15 | IRQ | IP | Interrupt request active low |
| 16 | A1 | OP | Address bit 1 |
| 17 | N/C | - | No Connection |
| 18 | E2 | OP | Enable 2 active high |
| 19 | GND | | Ground |
| 20 | +5V | | +5V Regulated Power |
| 21 | A3 | OP | Address bit 3 |
| 22 | A2 | OP | Address bit 2 |
| 23 | A5 | OP | Address bit 5 |
| 24 | A4 | OP | Address bit 4 |
| 25 | GND | | Ground |
| 26 | +5V | | +5V Regulated Power |

1.2 Connector Details

An industry standard, 26 way, dual in line, 0.1 inch pitch male header is used.



Top View of 26 way header

2. Revision History

| Issue | Date | Details | Author | Reviewed |
|-------|----------|-----------------------------------|--------|----------|
| 1 | 10/1/96 | First Issue | BMc | |
| 2 | 06/03/96 | Connector pin numbering corrected | BMc | |